

IC1					
MC_PE2	G4	B1-I/O1	B1-I/O13	H3	
RAM_D20	G3	B1-I/O2	B1-I/O14	E1	RAM_D23
RAM_D24	B2	B1-I/O3	B1-I/O15	F2	RAM_D21
	B1	B1-I/O4	B1-I/O16	F1	RAM_D22
	G5	B1-I/O5	B1-I/O17	J5	
RAM_ADR2	E4	B1-I/O6	B1-I/O18	H5	
RAM_DQM2	E3	B1-I/O7	B1-I/O19	J4	
RAM_D16	D2	B1-I/O8	B1-I/O20	H2	
DATA6	H7	B1-I/O9	B1-I/O21	H1	MC_PE3
	H6	B1-I/O10	B1-I/O22	J3	DRIVES_BSY
DATA13	J6	B1-I/O11	B1-I/O23	J2	SDC2_CD
	H4	B1-I/O12	B1-I/O24	J1	MC_PE4

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IC16\$2

DATA4	L6	B2-I/O1	B2-I/O18	U1	
DATA3	M6	B2-I/O2	B2-I/O19	V2	TP4
KEYB_RXD	M2	B2-I/O3	B2-I/O20	V1	RESET
KEYB_TXD	M1	B2-I/O4	B2-I/O21	P5	SEQ_C
	M4	B2-I/O5	B2-I/O22	N6	
	M3	B2-I/O6	B2-I/O23	R4	BOOT_ACK
SPI_MOSI	N2	B2-I/O7	B2-I/O24	R3	BOOT_REQ
SPI_SS0	N1	B2-I/O8	B2-I/O25	W2	TP6
	M5	B2-I/O9	B2-I/O26	W1	RESET_CORE
SPI_SST	P2	B2-I/O10	B2-I/O27	Y2	HSYNC
SPI_SS2	P1	B2-I/O11	B2-I/O28	Y1	USYNC
SPI_CLK	R2	B2-I/O12	B2-I/O29	T3	
SPI_MISO	R1	B2-I/O13	B2-I/O30	V4	
	N5	B2-I/O14	B2-I/O31	V3	
SEQ_D	P4	B2-I/O15	B2-I/O32	R5	
SEQ_Q	P3	B2-I/O16	B2-I/O33	T4	
TP5	U2	B2-I/O17	B2-I/O34	T5	

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IC16\$3

B7	V5	B3-I/O1	B3-I/O18	W8	R1
B8	Y4	B3-I/O2	B3-I/O19	AA7	G9
U_DE_B9	Y3	B3-I/O3	B3-I/O20	AB7	G8
B6	Y6	B3-I/O4	B3-I/O21	Y8	B4
VDAC_CLK	AA3	B3-I/O5	B3-I/O22	V9	B3
G0	AB3	B3-I/O6	B3-I/O23	V10	B2
R2	WA6	B3-I/O7	B3-I/O24	U10	
G3	AA4	B3-I/O8	B3-I/O25	AA8	R4
G2	AB4	B3-I/O9	B3-I/O26	AB8	R6
G5	AA5	B3-I/O10	B3-I/O27	AA9	R5
G7	AA6	B3-I/O11	B3-I/O28	AB9	R8
G6	AB6	B3-I/O12	B3-I/O29	U11	Not used
G4	AB5	B3-I/O13	B3-I/O30	V11	B0
R3	W7	B3-I/O14	B3-I/O31	W10	
B5	Y7	B3-I/O15	B3-I/O32	Y10	B1
G1	U9	B3-I/O16	B3-I/O33	AA10	R7
R0	V8	B3-I/O17	B3-I/O34	AB10	R9

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IC16\$4					
AA13		B4-I/O1	B4-I/O19	U15	SDC1_MISO
AB13		B4-I/O2	B4-I/O20	V15	
AA14		B4-I/O3	B4-I/O21	W15	
AB14		B4-I/O4	B4-I/O22	T15	DATA11
V12		B4-I/O5	B4-I/O23	AB18	
W13		B4-I/O6	B4-I/O24	AA18	TP1
Y13		B4-I/O7	B4-I/O25	AA19	MFP_TXD
AA15		B4-I/O8	B4-I/O26	AB19	TP2
AB15		B4-I/O9	B4-I/O27	W17	
U12		B4-I/O10	B4-I/O28	Y17	
Y14		B4-I/O11	B4-I/O29	AA20	TP3
Y15		B4-I/O12	B4-I/O30	AB20	MFP_RXD
AA16		B4-I/O13	B4-I/O31	V16	
AB16		B4-I/O14	B4-I/O32	U16	
V13		B4-I/O15	B4-I/O33	U17	SDC1_CLK
W14		B4-I/O16	B4-I/O34	T16	SDC1_MOSI
V14		B4-I/O17	B4-I/O35	R16	SDC1_PWR
U14		B4-I/O18	B4-I/O36	R14	DATA1

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IC16\$5

AA22	AA22	B5-I/O1	B5-I/O20	U21	DAC_SCLCK
AA21	AA21	B5-I/O2	B5-I/O21	R18	DATA0
SDC1_CD	T17	B5-I/O3	B5-I/O22	R19	ADR2
SDC1_WP	T18	B5-I/O4	B5-I/O23	R22	DAC_LDAC
W20	W20	B5-I/O5	B5-I/O24	R21	FLASH_OE
W19	W19	B5-I/O6	B5-I/O25	P20	ADR6
Y22	Y22	B5-I/O7	B5-I/O26	P22	ADR4
Y21	Y21	B5-I/O8	B5-I/O27	P21	ADR5
SDC1_D1	U20	B5-I/O9	B5-I/O28	N20	DATA8
SDC1_CS	U19	B5-I/O10	B5-I/O29	N19	ADR21
W22	W22	B5-I/O11	B5-I/O30	N18	DATA10
W21	W21	B5-I/O12	B5-I/O31	N22	ADR8
DAC_SDATA	T20	B5-I/O13	B5-I/O32	N21	ADR18
FLASH_CE	T19	B5-I/O14	B5-I/O33	M22	FLASH_RDY
ADR3	R17	B5-I/O15	B5-I/O34	M21	ADR7
	V22	B5-I/O16	B5-I/O35	M20	ADR22
	V21	B5-I/O17	B5-I/O36	M19	DATA9
ADR1	R20	B5-I/O18	B5-I/O37	M16	DATA2
DAC_SYNC	U22	B5-I/O19			

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IC16\$6

ADR19	L21	B6-I/O1	B6-I/O17	E21	CONFIG7
ADR12	K19	B6-I/O2	B6-I/O18	H18	ADR16
FLASH_RESET	K21	B6-I/O3	B6-I/O19	D22	CONFIG8
ADR14	J22	B6-I/O4	B6-I/O20	D21	ADR23
ADR9	J21	B6-I/O5	B6-I/O21	F20	DATA7
CONFIG1	H22	B6-I/O6	B6-I/O22	F19	LED4
ADR13	H21	B6-I/O7	B6-I/O23	G18	LED2
ADR20	K18	B6-I/O8	B6-I/O24	H17	ADR17
ADR11	J18	B6-I/O9	B6-I/O25	C22	CONFIG9
CONFIG4	F22	B6-I/O10	B6-I/O26	C21	RAM_D15
CONFIG5	F21	B6-I/O11	B6-I/O27	B22	CONFIG10
DATA12	J20	B6-I/O12	B6-I/O28	B21	RAM_D0
ADR15	J19	B6-I/O13	B6-I/O29	C20	RAM_D1
DATA5	H20	B6-I/O14	B6-I/O30	D20	RAM_D3
LED1	H19	B6-I/O15		F17	LED3
CONFIG6	E22	B6-I/O16			

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IC16\$7					
USB3_DM	E16	B7-I/O1	B7-I/O19	B17	RAM_DQM1
DATA14	F15	B7-I/O2	B7-I/O20	A16	RAM_ADR12
SDC2_MISO	F14	B7-I/O3	B7-I/O21	B16	PCF85363_SCL
SDC2_MOSI	C18	B7-I/O4	B7-I/O22	C15	RAM_D7
RAM_D4	D18	B7-I/O5	B7-I/O23	E14	USB2_DP
RAM_D5	D17	B7-I/O6	B7-I/O24	F13	SDC2_CS
RAM_D2	C19	B7-I/O7	B7-I/O25	A15	RAM_CKE
DATA15	D19	B7-I/O8	B7-I/O26	B15	SDC2_CLK
RAM_D13	A20	B7-I/O9	B7-I/O27	C13	RAM_DE
RAM_D14	B20	B7-I/O10	B7-I/O28	D13	RAM_DQM0
SDC2_PRR	C17	B7-I/O11	B7-I/O29	E13	USB2_DM
RAM_D12	B19	B7-I/O12	B7-I/O30	A14	RAM_ADR9
RAM_D11	A19	B7-I/O13	B7-I/O31	B14	RAM_ADR8
RAM_D9	A18	B7-I/O14	B7-I/O32	A13	RAM_ADR7
RAM_D10	B18	B7-I/O15	B7-I/O33	B13	RAM_ADR6
RAM_D6	D15	B7-I/O16	B7-I/O34	E12	USB1_PWR
USB3_DP	E15	B7-I/O17	B7-I/O35	E11	RAM_CS
RAM_D8	A17	B7-I/O18	B7-I/O36	F11	USB2_PWR

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IC16\$8

RAM_RAS	D10	B8-I/O1	B8-I/O19	B5	
RAM_ADR4	A10	B8-I/O2	B8-I/O20	F10	USB1_DM
RAM_ADR5	B10	B8-I/O3	B8-I/O21	C6	
RAM_DQM3	A9	B8-I/O4	B8-I/O22	D7	RAM_BA1
RAM_ADR3	B9	B8-I/O5	B8-I/O23	A4	RAM_D28
RAM_CS	C10	B8-I/O6	B8-I/O24	B4	RAM_D27
RAM_D30	A8	B8-I/O7	B8-I/O25	F8	PCF85363_INT
RAM_D31	B8	B8-I/O8	B8-I/O26	A3	RAM_D26
RAM_D29	A7	B8-I/O9	B8-I/O27	B3	RAM_D25
PCF85363_SDA	B7	B8-I/O10	B8-I/O28	D6	
SCC_TXD	A6	B8-I/O11	B8-I/O29	E7	RAM_D18
	B6	B8-I/O12	B8-I/O30	C3	RAM_ADR1
USB3_PWR	E9	B8-I/O13	B8-I/O31	C4	RAM_ADR0
RAM_BA0	C8	B8-I/O14	B8-I/O32	F7	PCF85363_TS
RAM_ADR10	C7	B8-I/O15	B8-I/O33	G7	
RAM_ADR11	D8	B8-I/O16	B8-I/O34	F9	USB1_DP
RAM_D17	E8	B8-I/O17	B8-I/O35	E6	RAM_D19
SCC_RXD	A5	B8-I/O18	B8-I/O36	E5	RAM_CLK

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FPGA (part 1)

TITLE: Suska-IV-B Series-Cyclone-2025.2

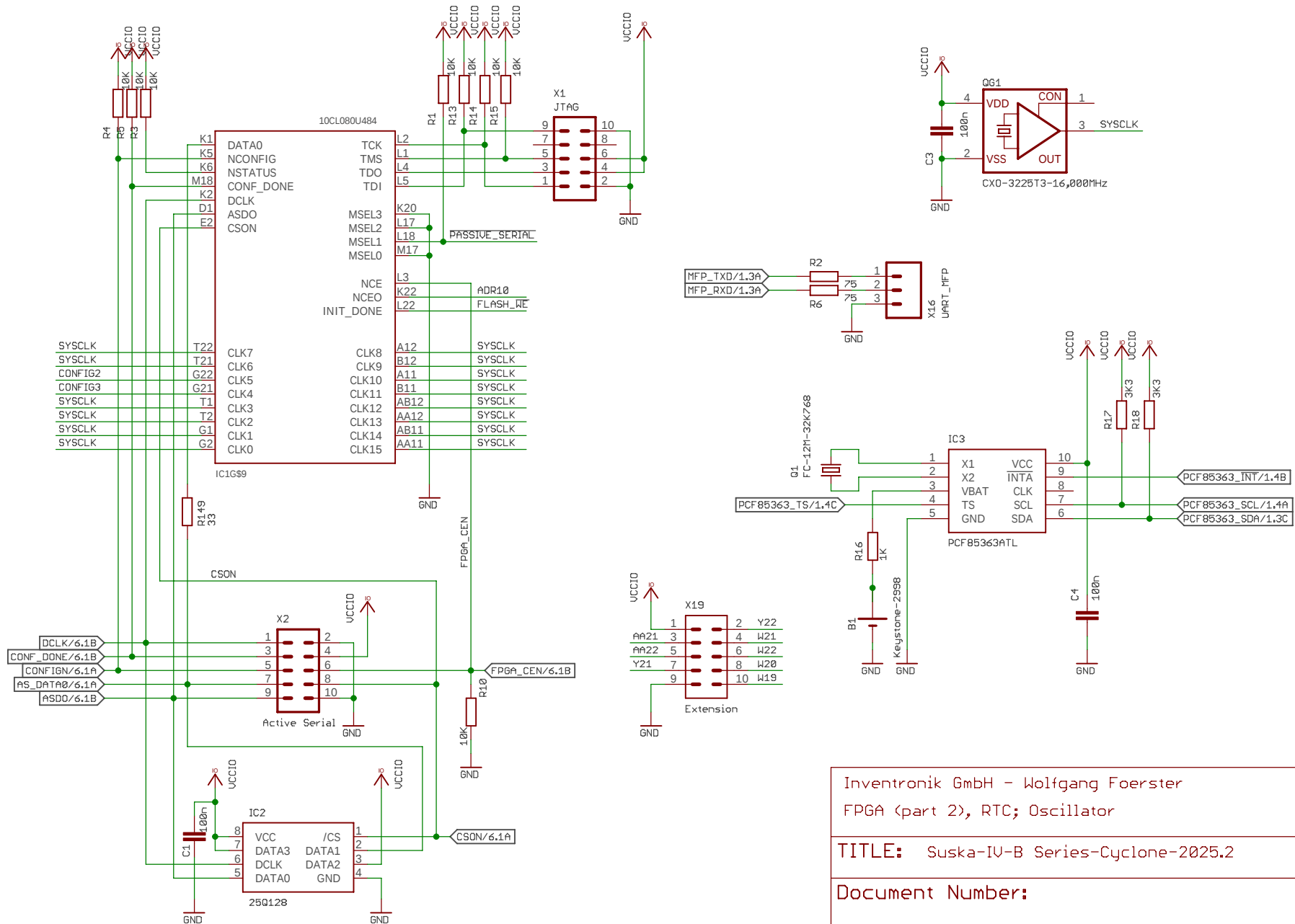
Document Number:

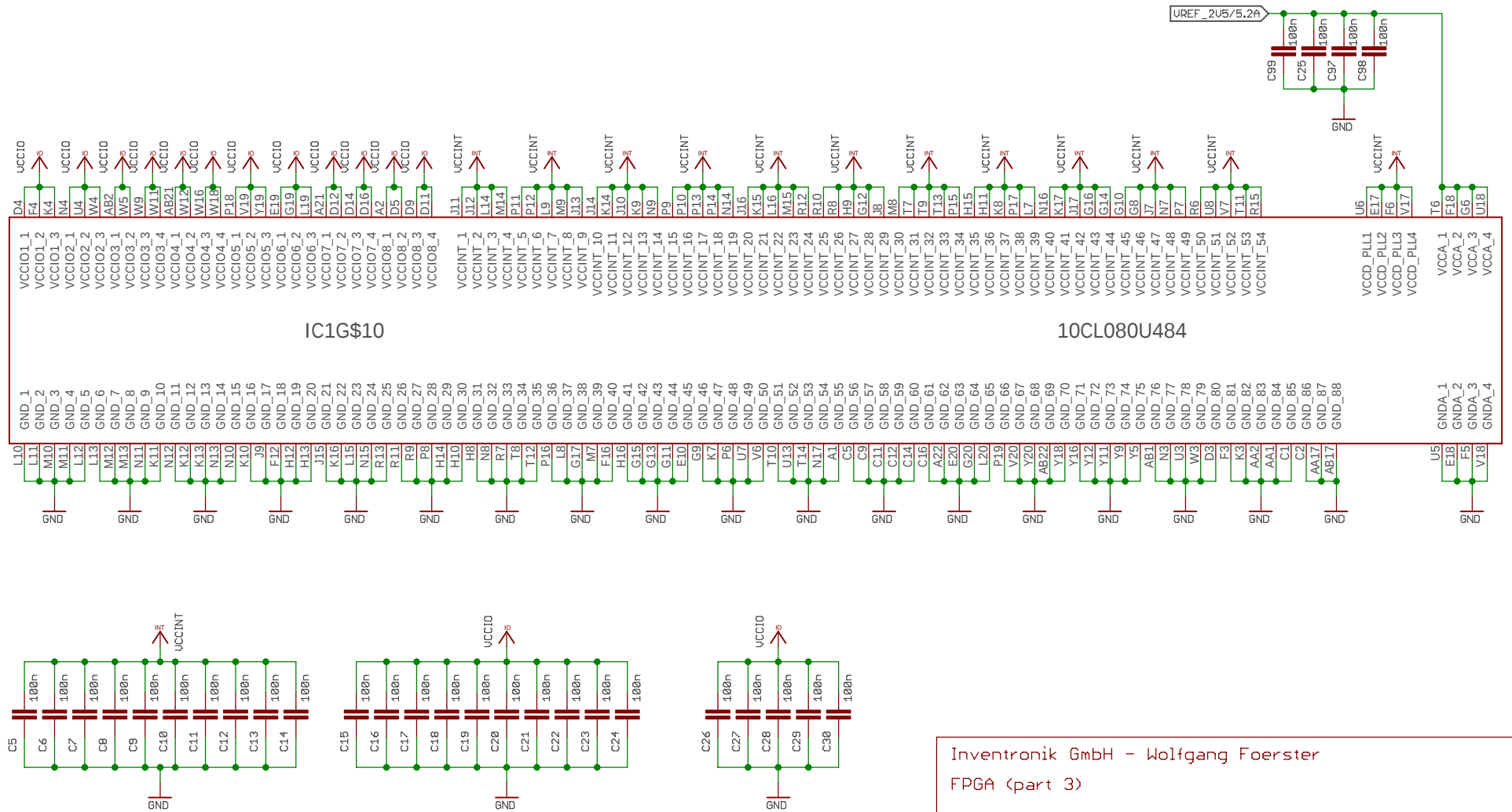
REV:

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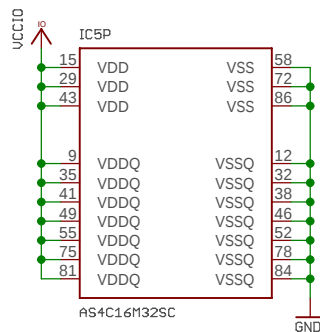
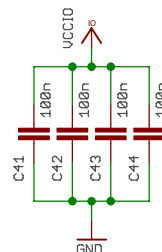
Date: 26.03.2025 17:31

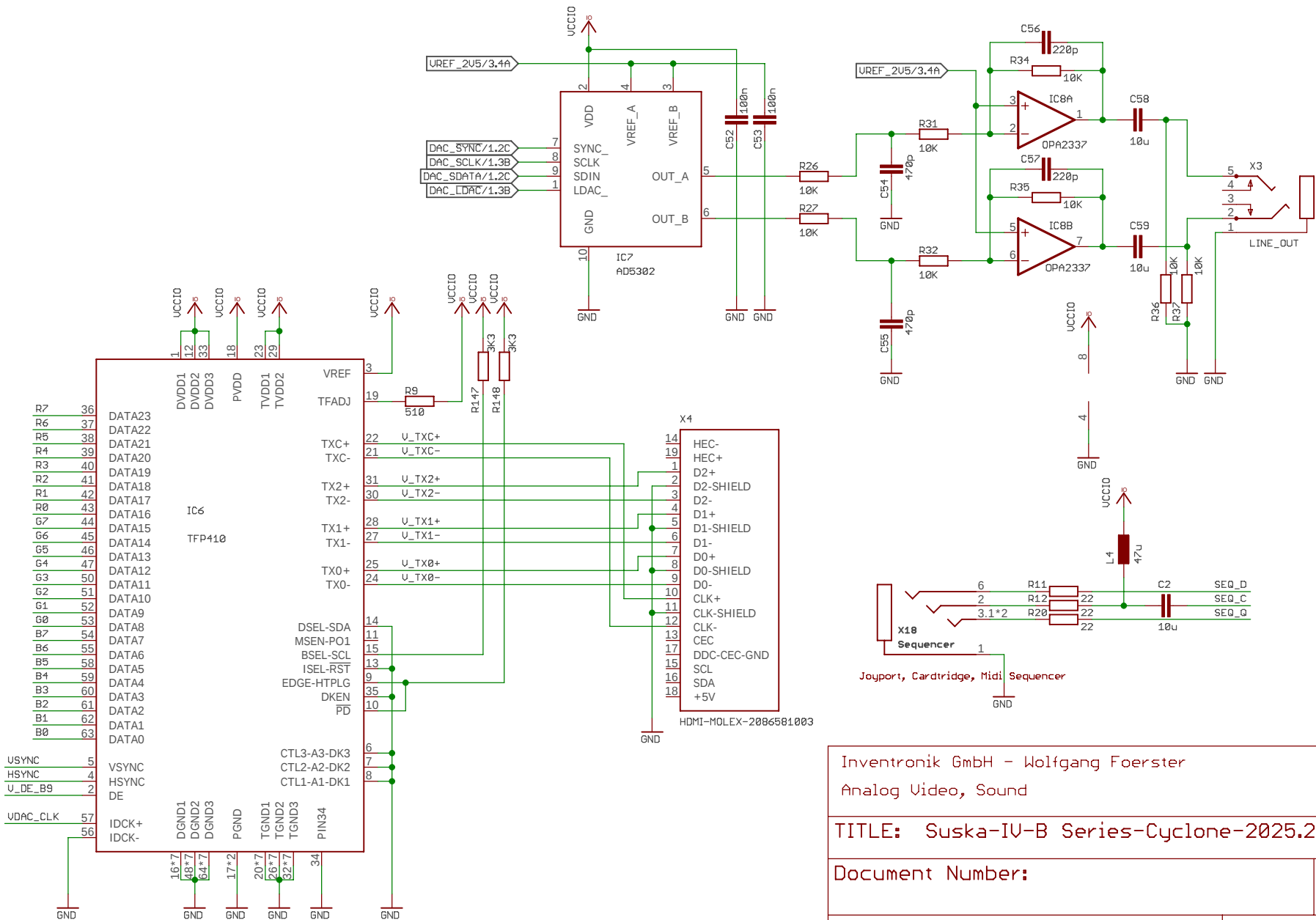
Sheet: 1/8



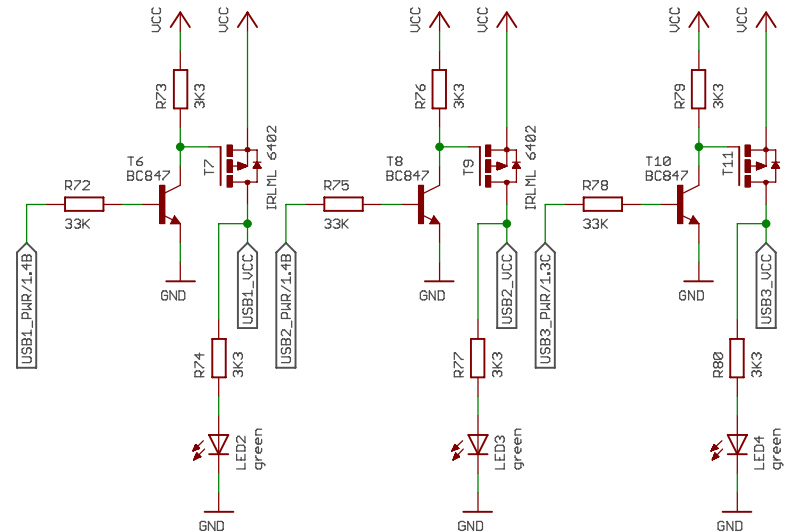
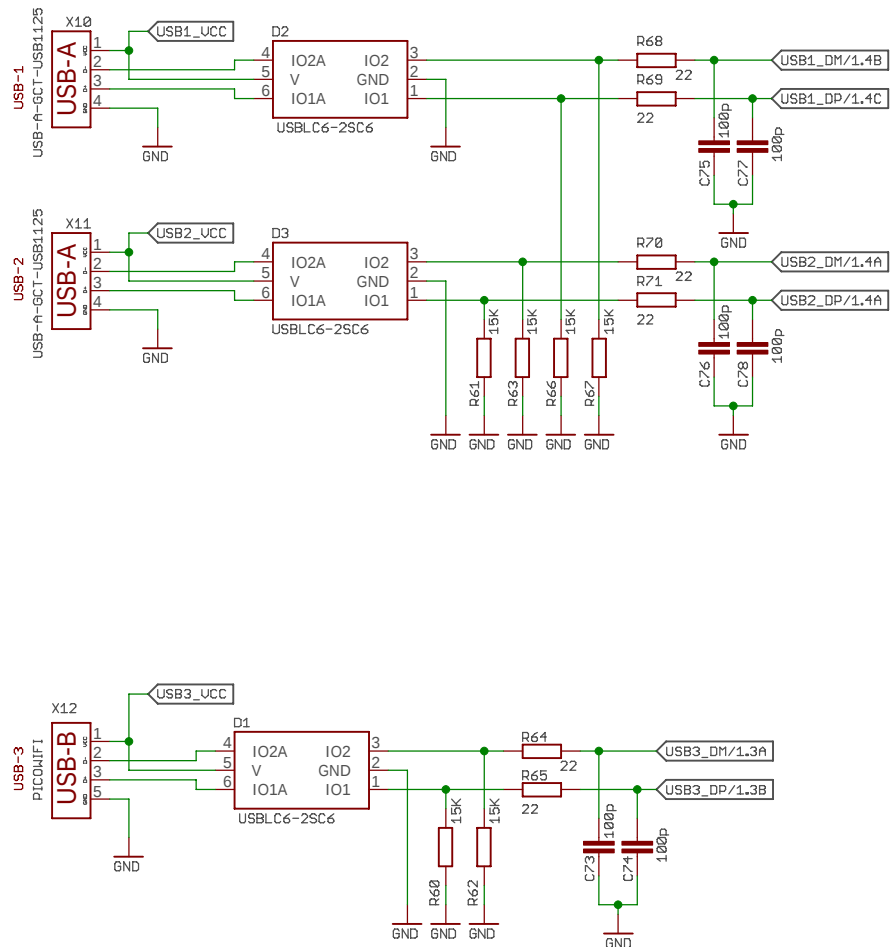


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FPGA (part 3)	
TITLE: Suska-IV-B Series-Cyclone-2025.2	
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TITLE: Suska-IV-B Series-Cyclone-2025.2	
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